

FIG. 1
 Prior Art

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Virtual to Physical Memory Address Translation and Protection Table	
Virtual Memory Addresses	Physical Memory Addresses
0X1000	8AEF000
0X22000	4D957000
0X46000	88AB42000
<hr/>	
0X123000	DE53C25000

FIG. 2
 Prior Art

Partition No.	Buffer size	Starting Address	Memory Allocation Bitmap Z2
Partition A	4KB	0X1000	00000001000000000000000000000010
Partition B	1KB	0X22000	0000000000000000000000000000000000
Partition C	256KB	0X4600	0000000000000000000000000000000000
⋮			
Partition N	128KB	0X123000	0000000000000000000000000000000000

FIG. 3
Prior Art

FIG. 4

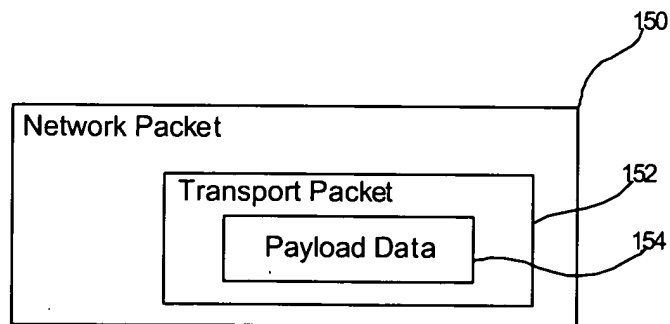
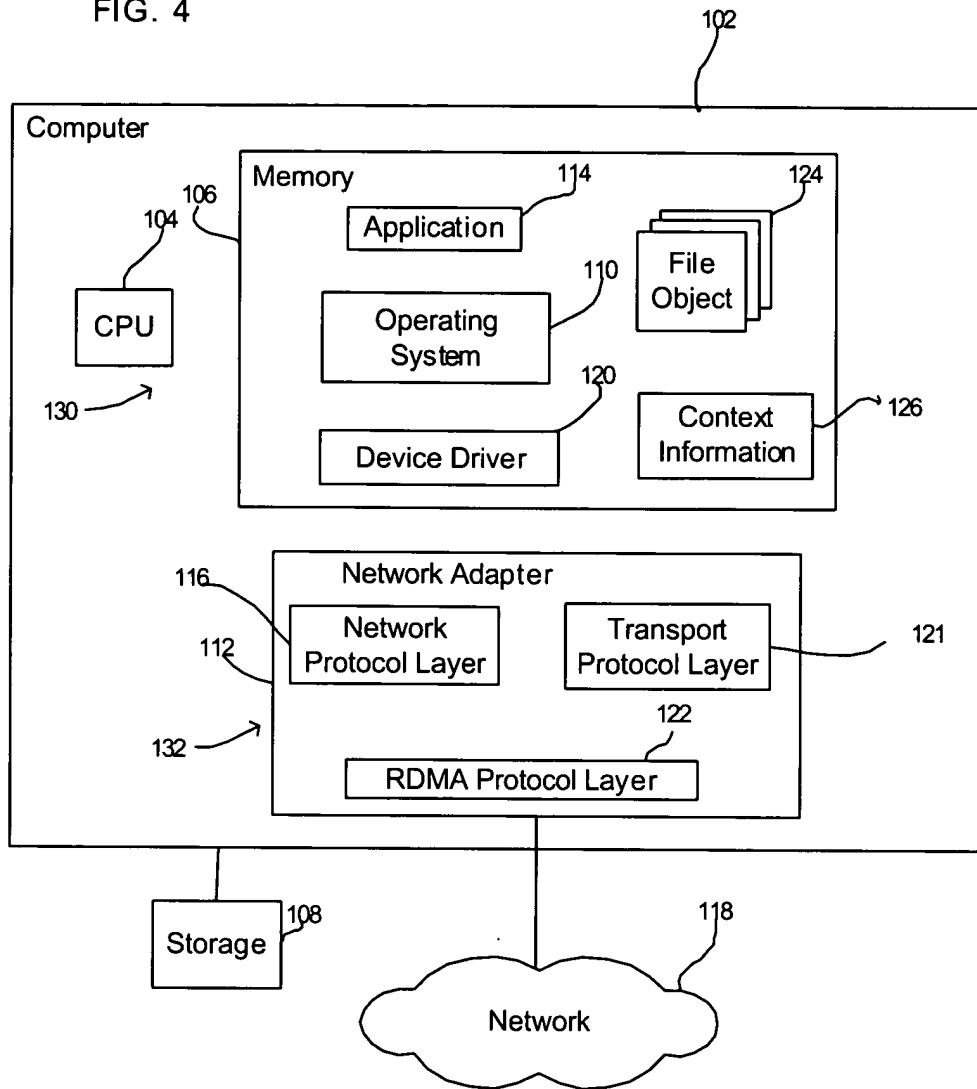


FIG. 5
Prior Art

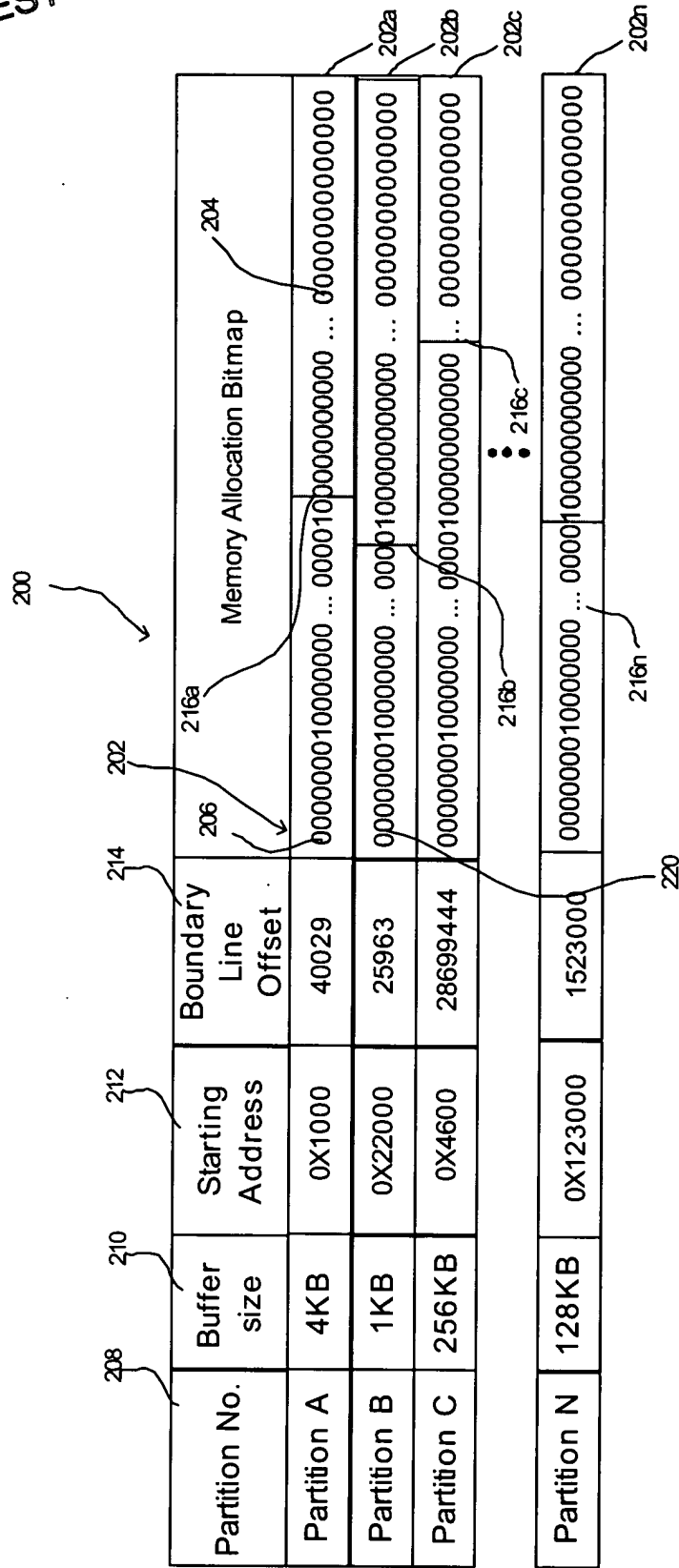


FIG. 6

METHOD, SYSTEM, AND PROGRAM FOR MANAGING
VIRTUAL MEMORY

Inventors: Harlan T. Beverly; Docket No. P17601

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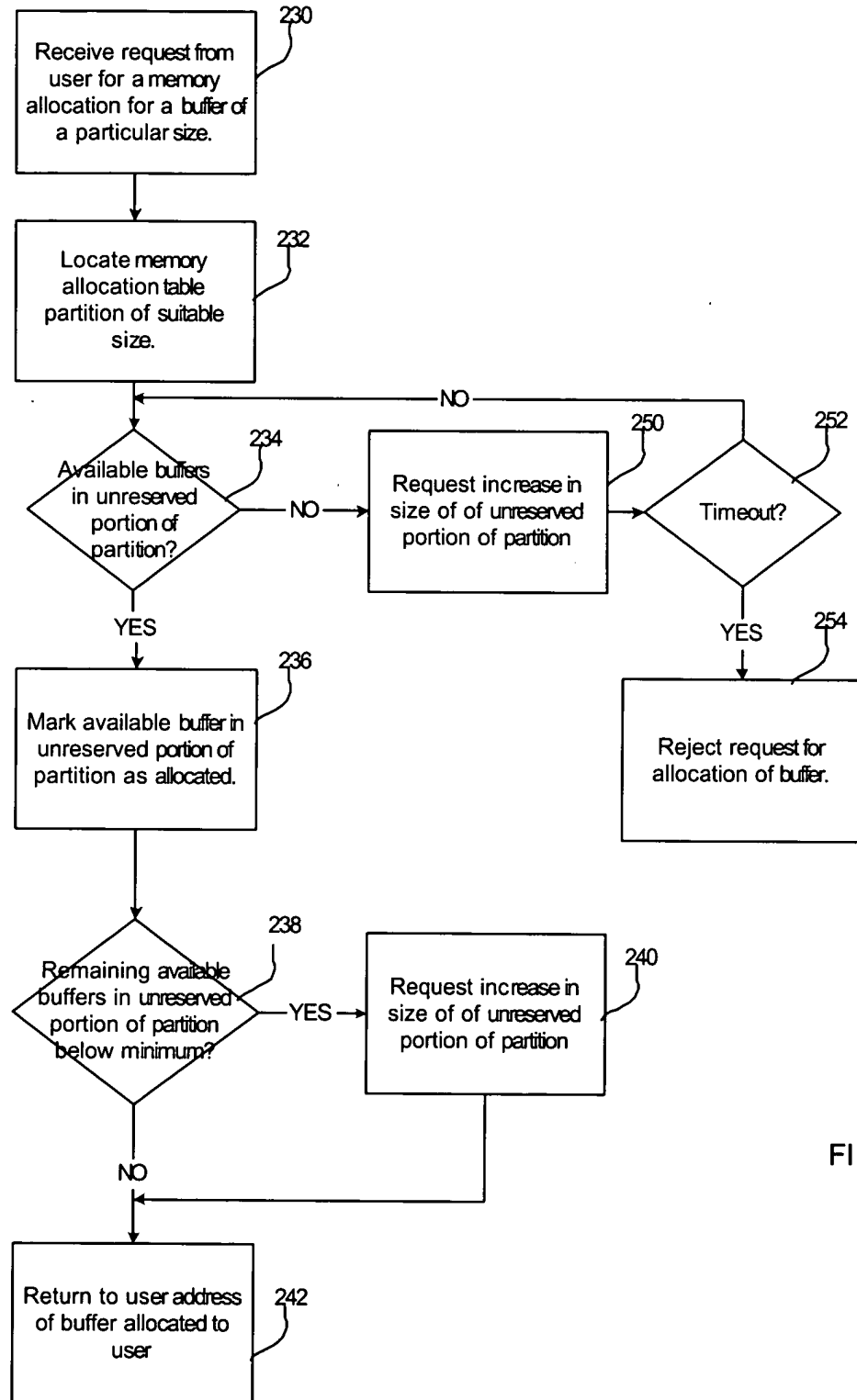


FIG. 7

METHOD, SYSTEM, AND PROGRAM FOR MANAGING
VIRTUAL MEMORY

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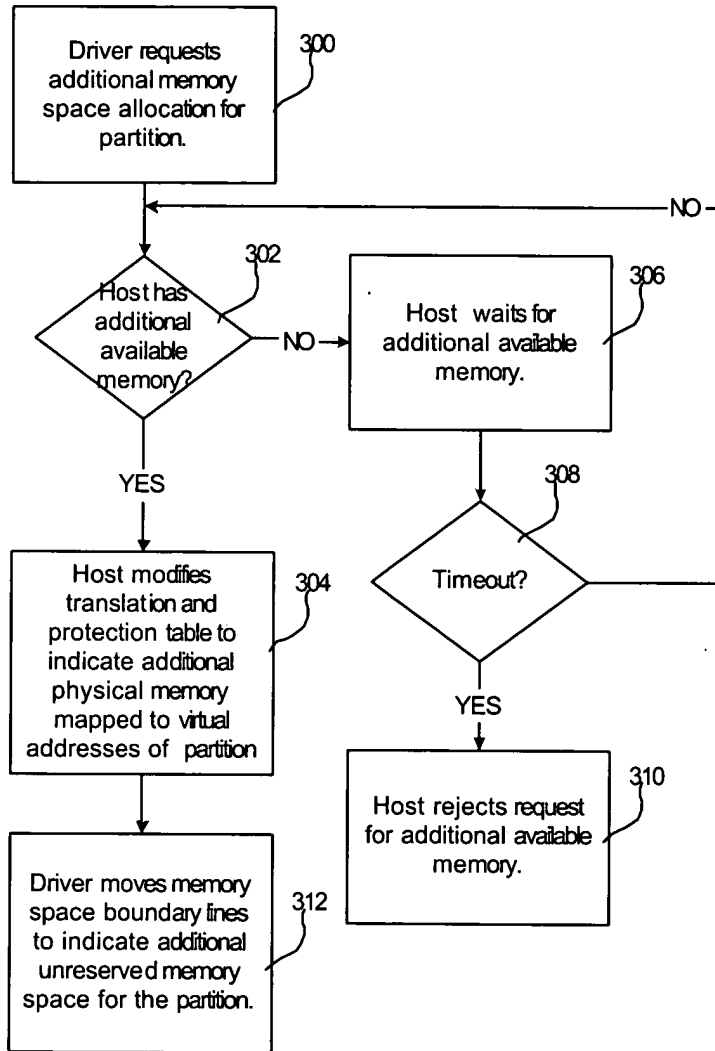


FIG. 8

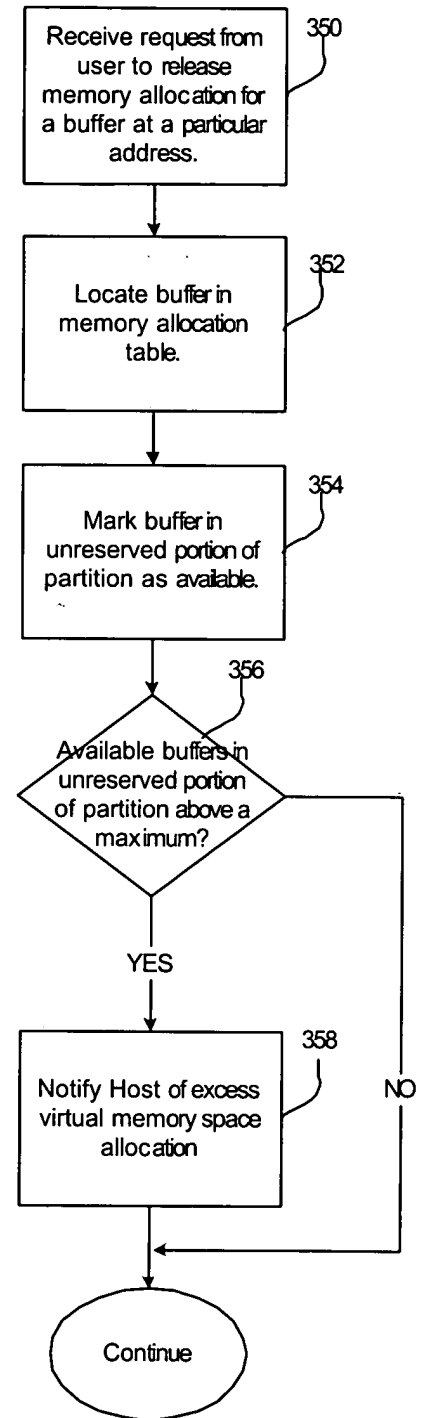


FIG. 9

METHOD, SYSTEM, AND PROGRAM FOR MANAGING
VIRTUAL MEMORY

Inventors: Harlan T. Beverly; Docket No. P17601

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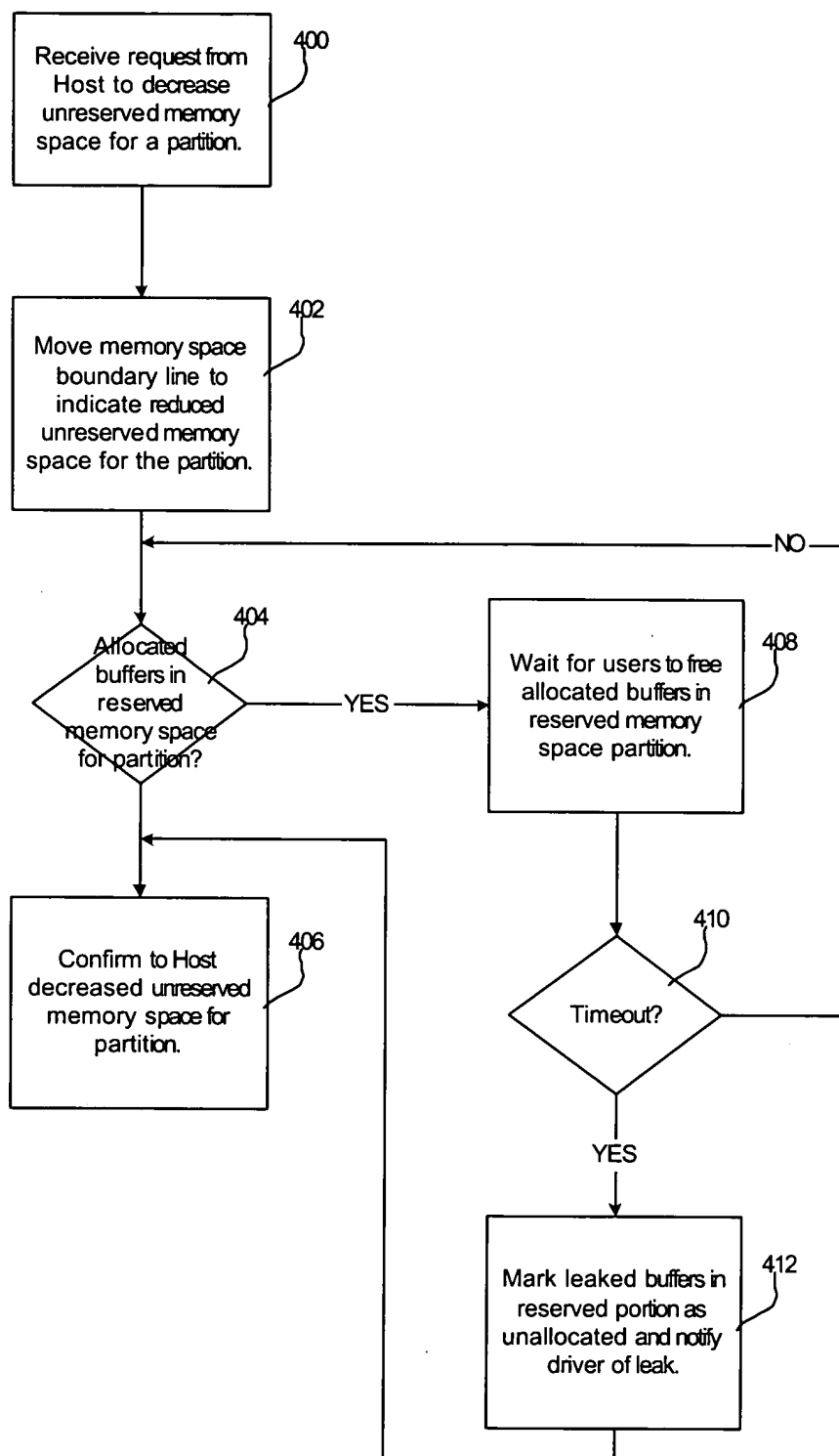


FIG. 10

METHOD, SYSTEM, AND PROGRAM FOR MANAGING
VIRTUAL MEMORY

Inventors: Harlan T. Beverly; Docket No. P17601

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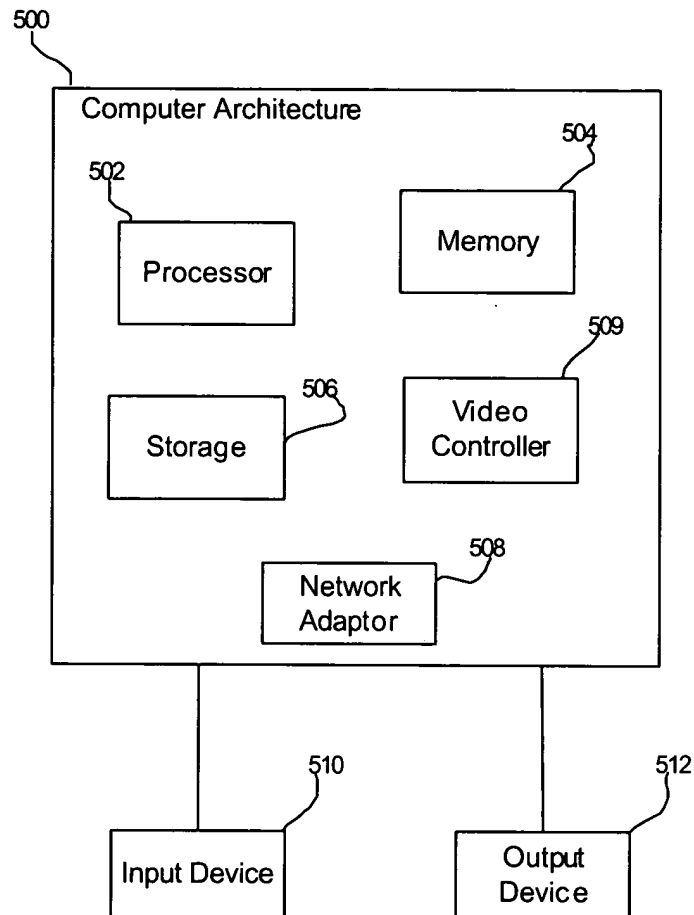


FIG. 11